



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nicola Telecco

PATENT APPLICATION

Serial No.: 10/666,324

Group Art Unit: 2838

Filed: September 17, 2003

Examiner:

For: DUAL STAGE VOLTAGE REGULATION CIRCUIT

Supplemental Information Disclosure Statement

Hon. Commissioner for Patents
Alexandria, VA 22313

Sir:

The following information is submitted in compliance with Applicant's duty of disclosure under 37 CFR § 1.56. A copy of the reference is enclosed.

Other Reference

Book entitled "Flash Memories" by Paolo Cappelletti et al., pp. 332-335.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Asst. Commissioner for Patents, Alexandria, VA 22313

Signed: Sally Azevedo
Typed Name: Sally Azevedo

Date: December 16, 2003

Respectfully submitted,

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FORM PTO-1449

Atty. Docket No.
ATM-228Serial No.
10/666,324LIST OF PRIOR ART
CITED BY APPLICANTApplicant:
Nicola TeleccoFiling Date:
Sept. 17, 2003Group:
2838

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Grant Date	Name	Class	Sub Class	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Document Number	Grant Date	Country	Class	Sub Class	Translation Yes No
AK						
AL						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AM	Book entitled "Flash Memories" by Paolo Cappelletti et al., pp. 332-335.
AN	
AO	

EXAMINER:

DATE CONSIDERED:

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.